

Serial No. 10/052,801

Reply to Office Action of April 1, 2005

Amendments to the Specification:

CP 2/22/07
26
Please replace paragraph [0032] with the following amended paragraph:

20
[0032] FIG. 1 is a block diagram of a conventional feed-forward linear amplifier. An input signal is applied to coupler 100a [10a] which couples portions of the input signal to delay line 140 and to main amplifier 110. Main amplifier 110 produces an amplified output having intermodulation products generated due to non-linearities in main amplifier 110. A portion of the amplified output signal is coupled to summer 150 by coupler 100b. Delay line 140 delays the input signal with respect to the output of the amplifier 110 producing a delayed signal such that the two signals reach summer 150 at approximately the same time, but reversed in phase.

CP 2/22/07
48
Please replace paragraph [0060] with the following amended paragraph:

48
[0060] The adjustment process is typically initiated by the amplifier control system during typical operation. At the start of the process 451, the ratio A/C of the spurious components is measured 453. Next the predistortion circuit is adjusted 455 in a manner to minimize the previously measured ratio. Next the distortion ratio B/A [A/B] is measured 457. After the measurement the phase and gain circuits are adjusted 459 in a manner that tends to reduce the distortion. The adjustment process needs to run continuously, since the amplifier response is constantly changing. Thus, the process loops back to the start 451.